

FIG.1

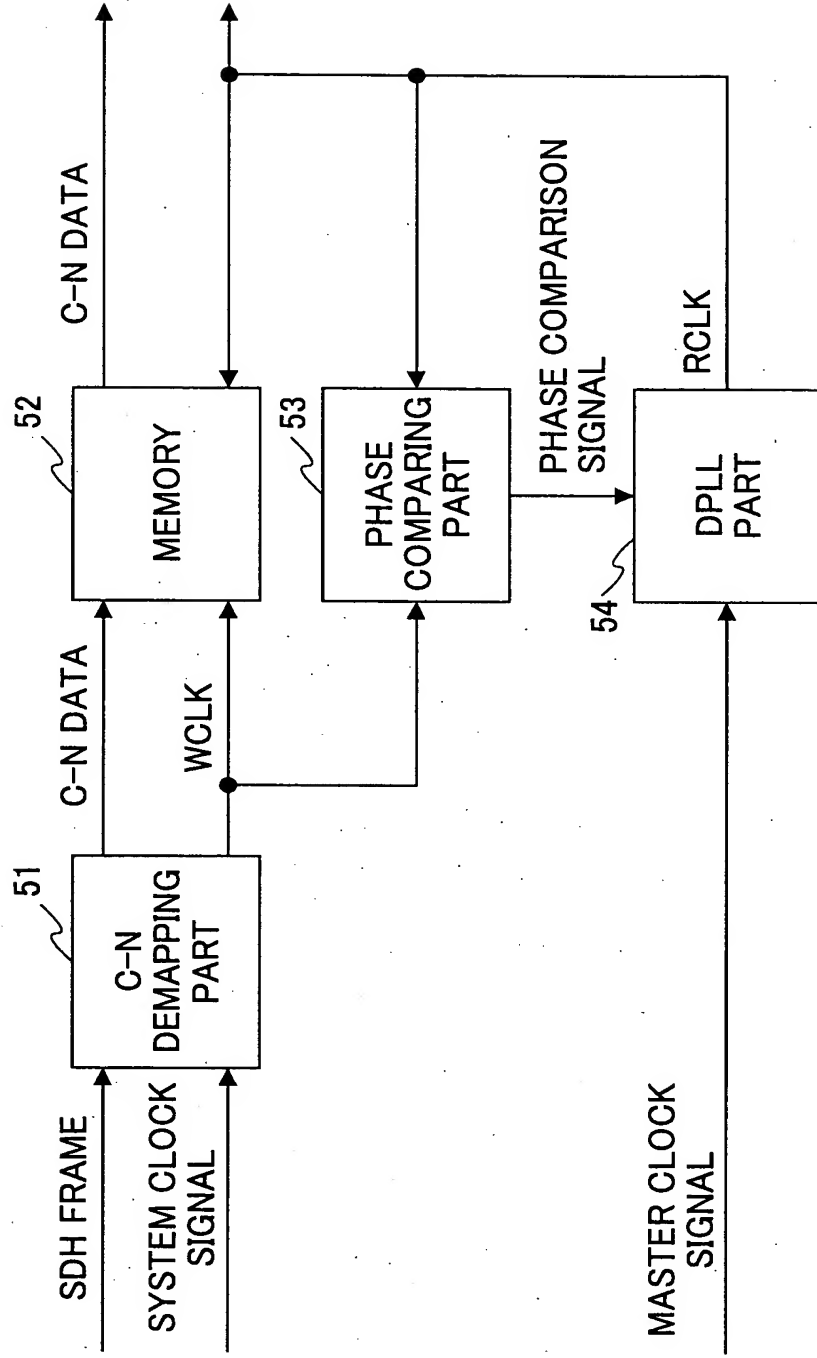


FIG.2

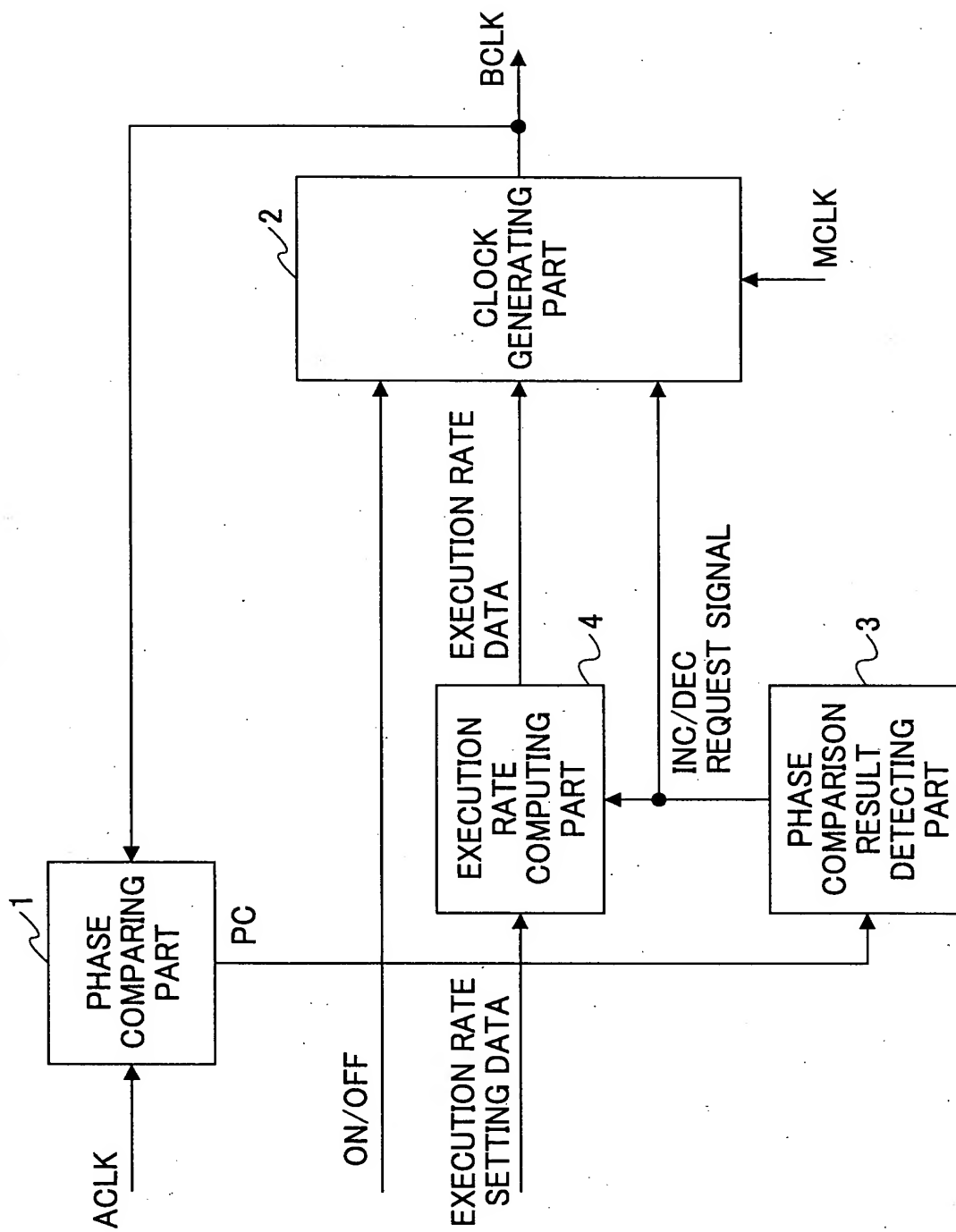
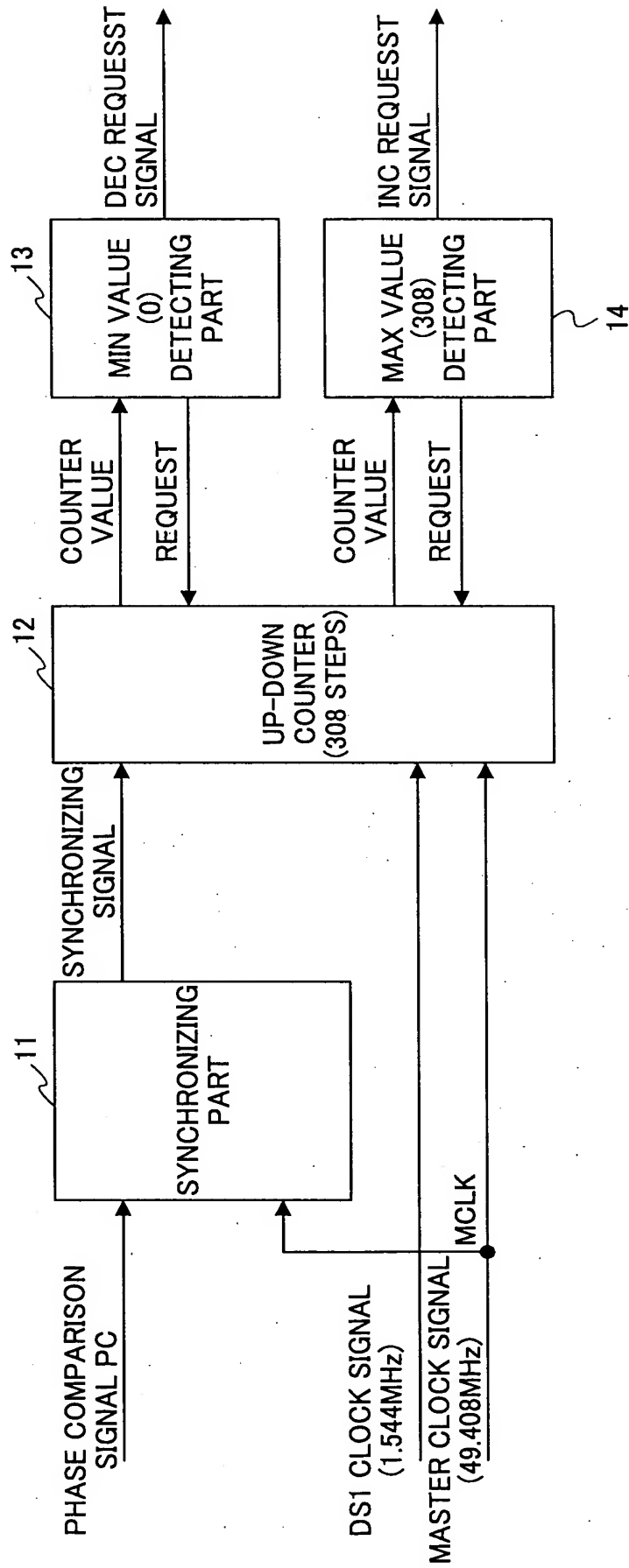


FIG.3



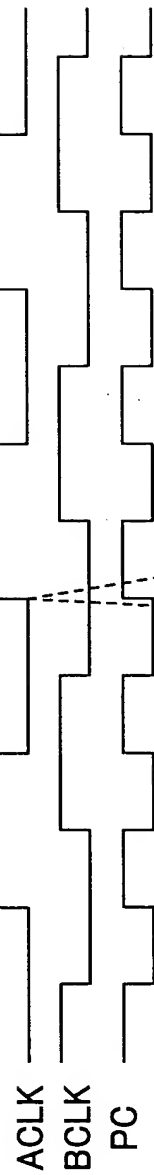


FIG. 4A

(ACLK GOES BEHIND BCLK)

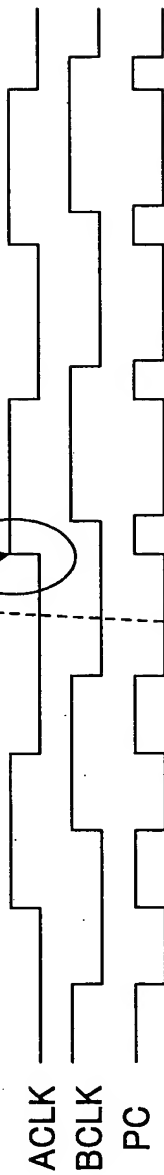


FIG. 4B

(ACLK GOES AHEAD BCLK)

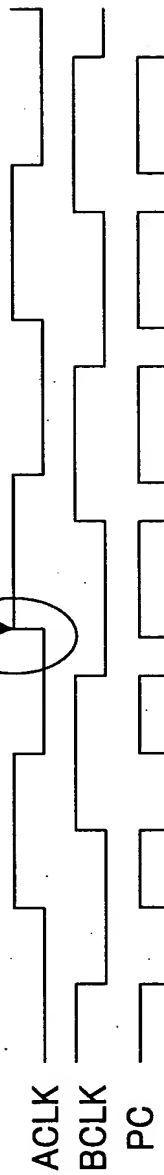


FIG. 4C

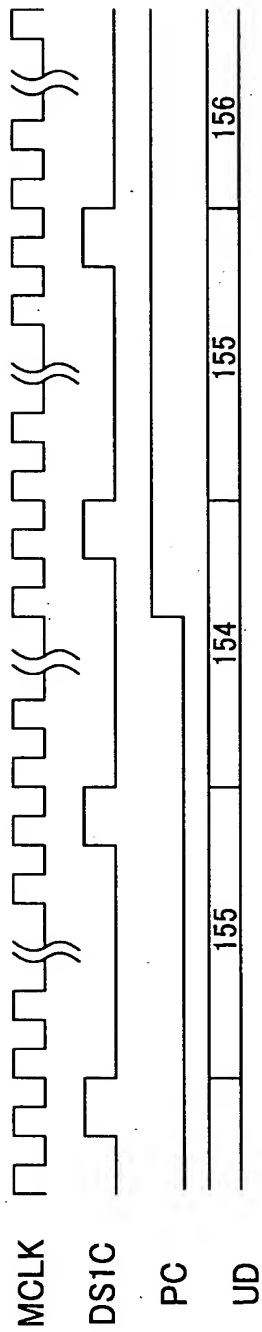


FIG.5A

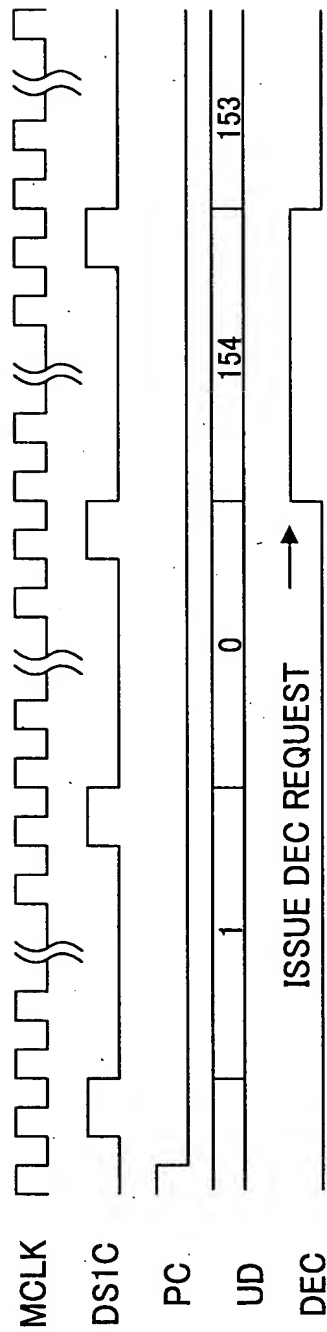


FIG.5B

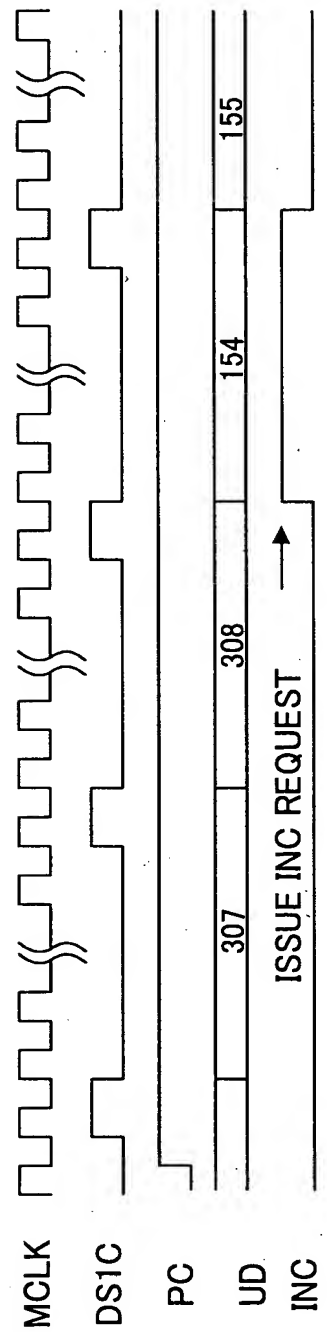


FIG.5C

FIG.6

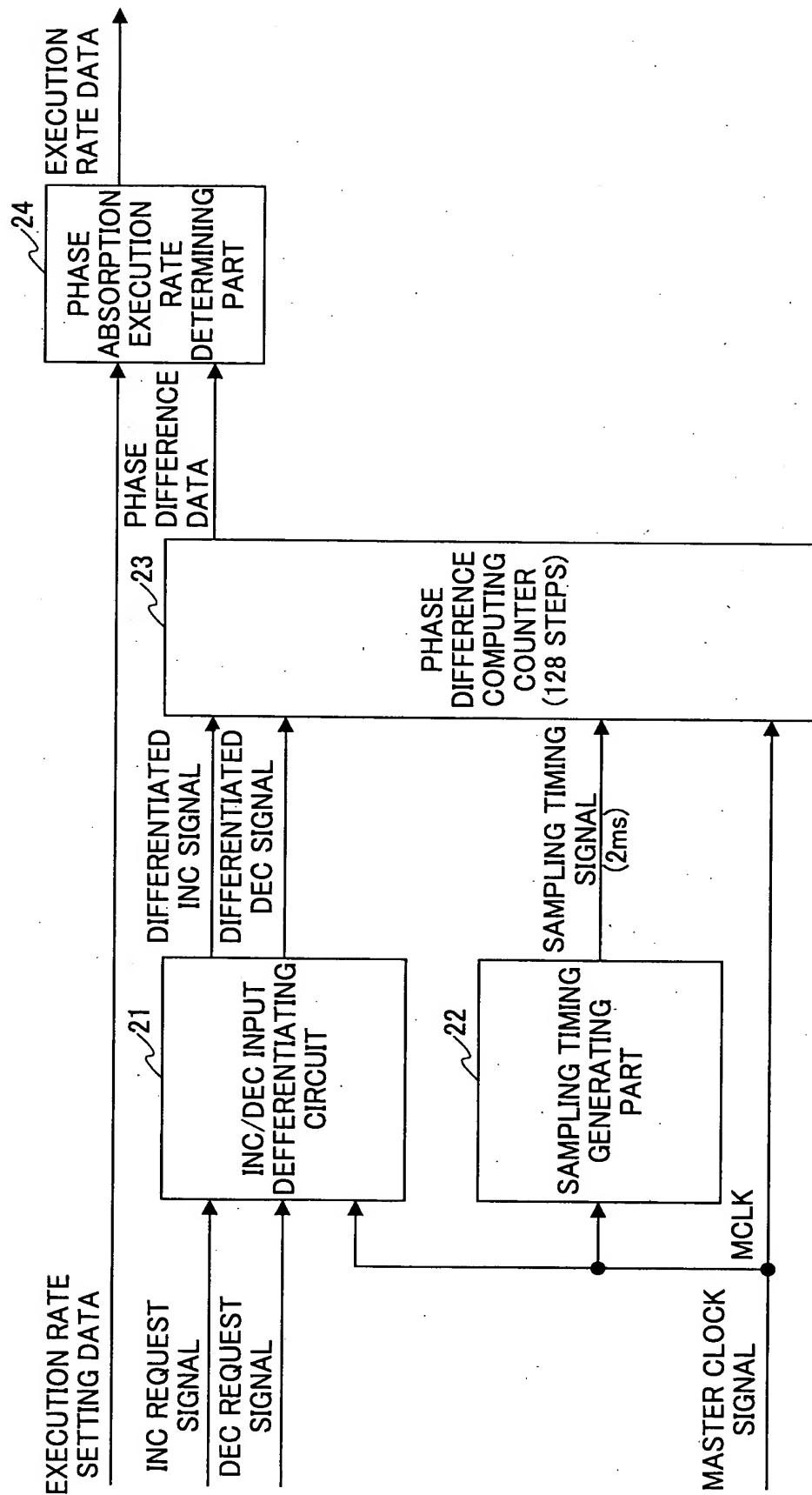


FIG.7

PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE
50bit	1/1	40bit	1/1	30bit	3/4	20bit	1/2	10bit	1/2
49bit	1/1	39bit	1/1	29bit	3/4	19bit	1/2	9bit	1/2
48bit	1/1	38bit	1/1	28bit	3/4	18bit	1/2	8bit	1/2
47bit	1/1	37bit	1/1	27bit	3/4	17bit	1/2	7bit	1/2
46bit	1/1	36bit	1/1	26bit	1/2	16bit	1/2	6bit	1/4
45bit	1/1	35bit	3/4	25bit	1/2	15bit	1/2	5bit	1/2
44bit	1/1	34bit	3/4	24bit	1/2	14bit	1/2	4bit	1/2
43bit	1/1	33bit	3/4	23bit	1/2	13bit	3/4	3bit	1/2
42bit	1/1	32bit	3/4	22bit	1/2	12bit	3/4	2bit	1/2
41bit	1/1	31bit	3/4	21bit	1/2	11bit	3/4	1bit	1/2

FIG.8A

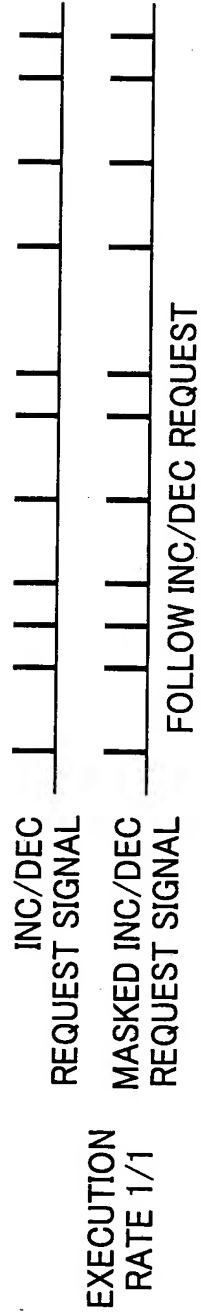


FIG.8B

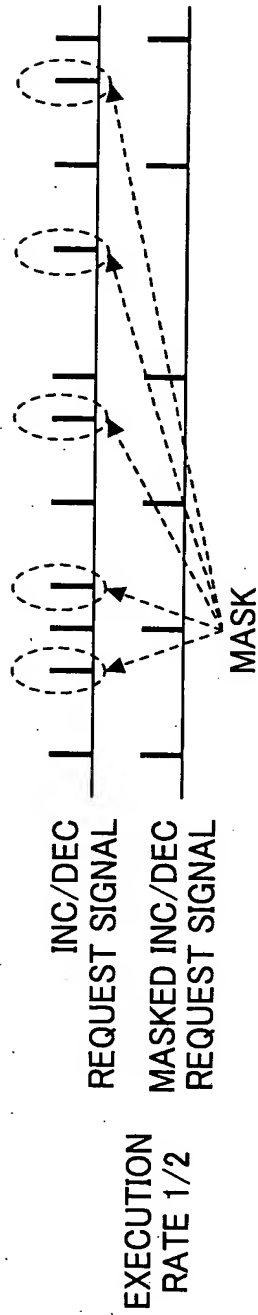


FIG.8C

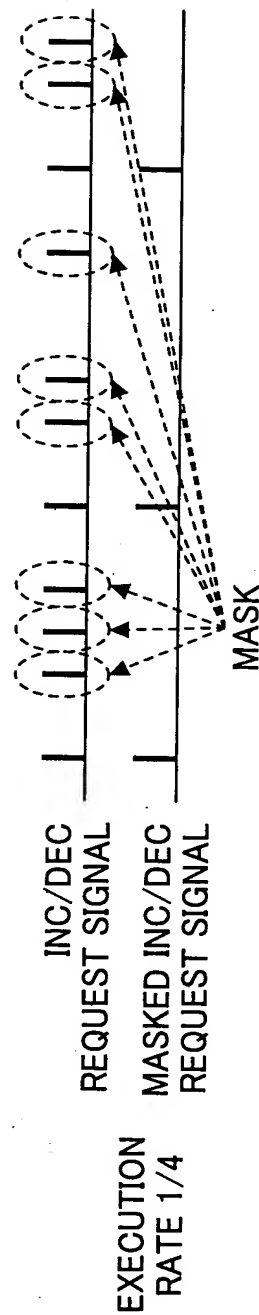


FIG.8D

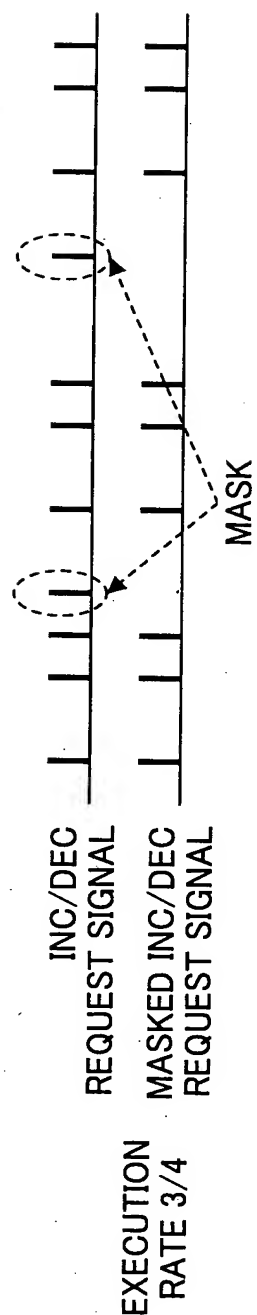




FIG.9

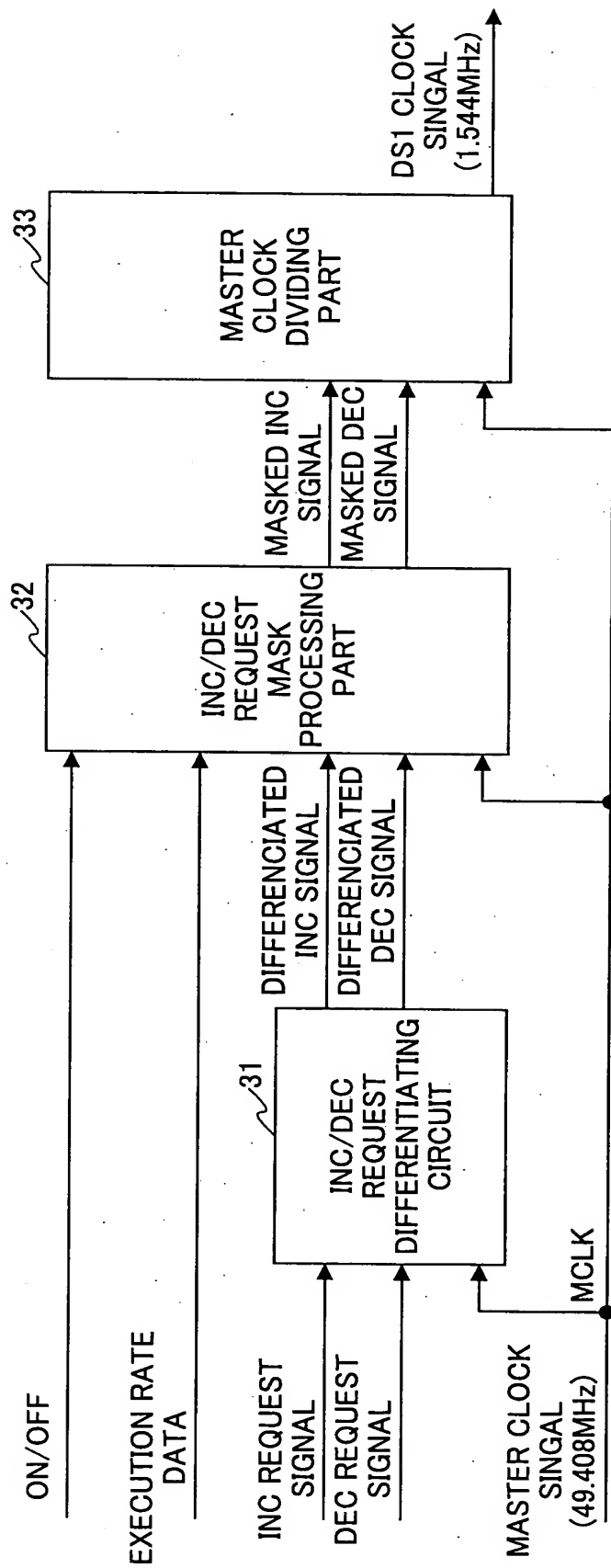


FIG.10A

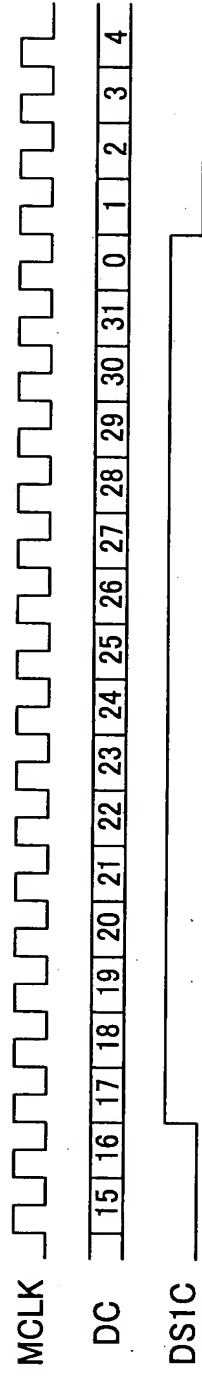


FIG.10B

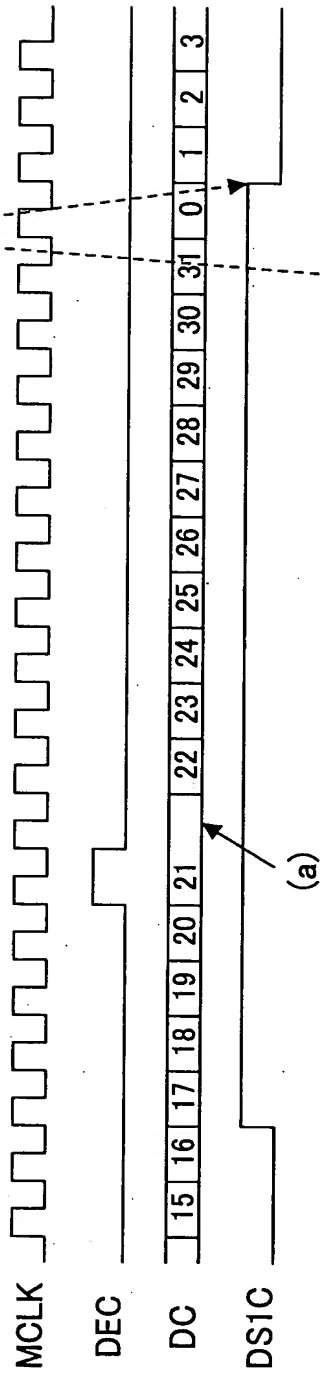


FIG.10C

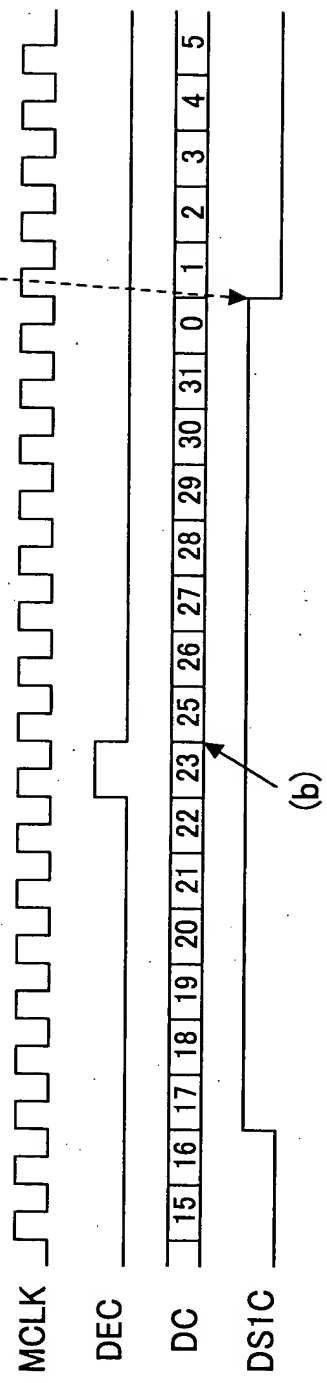


FIG.11A

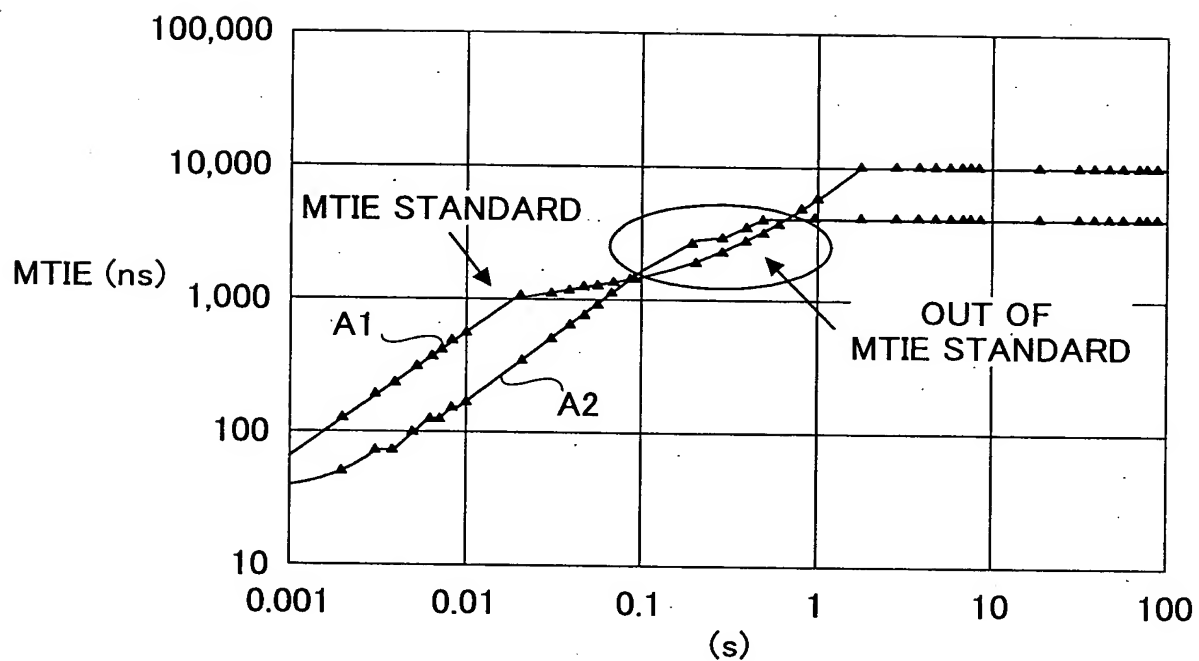


FIG.11B

